



AMENDMENTS TO THE SPECIFICATION

Please replace the Title of the Invention with the following:

CIRCUIT AND METHOD FOR PROVIDING A PRECISE CLOCK FOR DATA
COMMUNICATIONS

Please replace the paragraph beginning on page 2, line 10 with the following paragraph:

Conventional Universal Serial Bus (USB) communication systems are PLL based, with an accurate local clock. The USB device can be compliant with the USB specification version 2.0 (published April 2000), which is hereby incorporated by reference in its entirety (~~see http://www.usb.org/developers/usb20/~~). USB version 2.0 host Full-speed communications packets include accurately spaced 'Start Of Frame' (SOF) packets that arrive periodically every 1 ms $\pm 0.05\%$ (i.e., 1.000 ms \pm 500 ns) (see USB specification version 2.0, sections 7.1.11 and 7.1.12).

Please replace the paragraph beginning on page 15, line 19 with the following paragraph:

An alternative embodiment of the present invention may use pulses generated in response to the arrival of the SOF packets as the reference signal of a phase locked loop (PLL). The PLL would generally lock to the SOF packet rate. However, the use of

a PLL ~~involved addition~~ involves additional design to achieve the desired accuracy in the required time (e.g., ~10 ms).